

**PC, DVI INTERFACE CONTROLLER
FOR TFT PANEL**

Model: ALR-1400

Part number : 41710002X-3

INSTRUCTIONS

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It is essential that these instructions are read and understood before connecting or powering up this controller.

Introduction

Designed for LCD monitor and other flat panel display applications, the ALR-1400 controller provides easy to use interface controller for:

- TFT (active matrix) LCDs with LVDS interface of 1366x768, 1280x1024, 1280x768, 1024x768, 800x600, 640x480 resolution;
- Computer video signals of SXGA, XGA, SVGA, VGA standard
- Support LVDS or TTL interface panel

HOW TO PROCEED

- Ensure you have all parts & that they are correct, refer to:
 - Connection diagram (separate document for each panel)
 - Connector reference (in following section)
 - Assembly notes
- Check controller switch & jumper settings (errors may damage the panel)
- Prepare the PC
- Connect the parts
- Understand the operation and functions (in following section)

IMPORTANT USAGE NOTE

This product is for use by system developers and integrators, the manufacturer accepts no liability for damage or injury caused by the use of this product. It is the responsibility of the developer, integrators or other user of this product to:

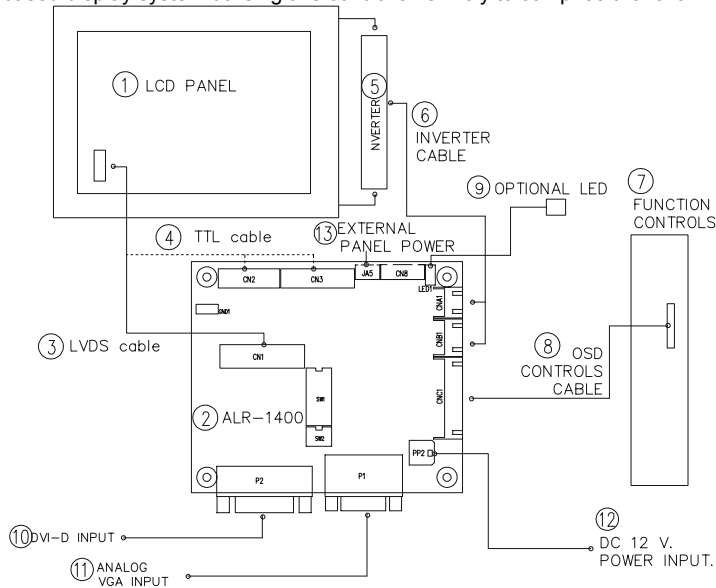
- Ensure that all necessary and appropriate safety measures are taken.
- Obtain suitable regulatory approvals as may be required.
- Check power settings to all component parts before connection.
- Understand the operation and connectivity requirements of this controller.

DISCLAIMER

There is no implied or expressed warranty regarding this material.

SYSTEM DESIGN

A typical LCD based display system utilising this controller is likely to comprise the following:



Summary:

1. LCD panel
2. LCD controller card, ALR-1400
3. LVDS cable (for connection with LVDS panel)
4. TTL cable (for connection with TTL panel)
5. Inverter for CCFT backlight (if not built into LCD)
6. Inverter cable
7. Function controls
8. Function controls cable
9. Status LED
10. DVI-D input
11. Analog VGA input
12. Power input (12VDC)
13. External panel power input (for 12V~18V panel power panel only)

Digital View offers a range of accessories such as listed above, to make up complete display solution.

ASSEMBLY NOTES

This controller is designed for monitor and custom display projects using 1366 x 768, 1280 x 1024 or 1280 x 768 or 1024 x 768 or 800x600 or 640x480 resolution TFT panels with a VGA, SVGA, XGA, SXGA signal input. The following provides some guidelines for installation and preparation of a finished display solution.

Preparation: Before proceeding it is important to familiarize yourself with the parts making up the system and the various connectors, mounting holes and general layout of the controller. As much as possible connectors have been labeled. Guides to connectors and mounting holes are shown in the following relevant sections.

1. **LCD Panel:** This controller is designed for typical LVDS or TTL interface TFT panels with panel voltage 3.3V or 5V or 12V, External for 12V~18V LVDS interface. Due to the variation between manufacturers of signal timing and other panel characteristics factory setup and confirmation should be obtained before connecting to a panel. **(NOTE: Check panel power jumper settings before connection)**
2. **Controller card:** Handle the controller card with care as static charge may damage electronic components.
3. **LVDS signal cable :** In order to provide a clean signal it is recommended that LVDS signal cables are no longer than 46cm (18 inches). If loose wire cabling is utilized these can be made into a harness with cable ties. Care should be taken when placing the cables to avoid signal interference. Additionally it may be necessary in some systems to add ferrite cores to the cables to minimize signal noise.
4. **TTL signal cable:** In order to provide a clean signal it is recommended that LCD signal cables are no longer than 33cm (13 inches). If loose wire cabling is utilized these can be made into a harness with cable ties. Care should be taken when placing the cables to avoid signal interference. Additionally it may be necessary in some systems to add ferrite cores to the cables to minimize signal noise.
5. **Inverter:** This will be required for the backlight of an LCD, some LCD panels have an inverter built in. As panels may have 1 or more backlight tubes and the power requirements for different panel models backlights may vary it is important to match the inverter in order to obtain optimum performance. See page 15 for the Application notes "Inverter connection section for more informations.
6. **Inverter Cables:** Different inverter models require different cables and different pin assignment. Make sure correct cable pin out to match the inverter. Using wrong cable pin out may damage the inverter.
7. **Function Controls:** The following section discusses the controls required and the section on connectors provides the detail. The controls are minimal: On/Off, Backlight Brightness (depends on inverter), OSD (5 momentary buttons) analog VR type or (8 momentary buttons) digital type.
8. **Function controls cable:** The cables to the function switches should be of suitable quality and length so that impedance does not affect performance. Generally lengths up to 1 metre (3 feet) should be acceptable.
9. **DVI-D Input Cable :** Plug the DVI cable to the connector P2 on the controller board
10. **Analog VGA Input Cable:** As this may affect regulatory emission test results and the quality of the signal to the controller, a suitably shielded cable should be utilized.
 - **Power Input:** 12V DC is required, this should be a regulated supply. Although the controller provides power regulation for the LCD power this does not relate to the power supplied to the backlight inverter. If an unregulated power supply is provided to an inverter any fluctuations in power may affect operation, performance and lifetime of the inverter and or backlight tubes.
 - **Power Safety:** Note that although only 12VDC is supplied as 'power-in' a backlight inverter for panel backlighting produces significantly higher voltages (the inverter does not connect to the ground plane). We strongly advise appropriate insulation for all circuitry.
 - **EMI:** Shielding will be required for passing certain regulatory emissions tests. Also the choice of external Controller to PC signal cable can affect the result.
 - **Ground:** The various PCB mounting holes are connected to the ground plane.
 - **Servicing:** The board is not user serviceable or repairable. Warranty does not cover user error in connecting up to the controller and is invalidated by unauthorized modification or repairs.
 - **Controller Mounting:** It is recommended that a clearance of at least 10mm is provided above and 5mm below the controller when mounted. Additionally consideration should be given to:
 - Electrical insulation.
 - Grounding.
 - EMI shielding.
 - Cable management. Note: It is important to keep panel signal cables apart from the inverter & backlight cables to prevent signal interference.
 - Heat & Ventilation: Heat generated from other sources, for example the backlight of a very high brightness panel may generate significant heat which could adversely affect the controller.
 - Other issues that may affect safety or performance.

- **PC Graphics Output:** A few guidelines:
 - Signal quality is very important, if there is noise or instability in the PC graphics output this may result in visible noise on the display.
 - Refer to graphics modes table in specifications section for supported modes.
 - Non-interlaced & interlaced video input is acceptable.

IMPORTANT: Please read the Application Notes section for more information.

CONNECTION & OPERATION

CAUTION: Never connect or disconnect parts of the display system when the system is powered up as this may cause serious damage.

CONNECTION

Connection and usage is quite straight forward (it is useful to have the relevant connection diagram available at this time):

1. **LCD panel & Inverter:** Connect the inverter (if it is not built-in the panel) to the CCFT lead connector of the LCD panel.
2. **LVDS type panels:** Plug the LVDS signal cable direct to CN1 (if necessary). Insert the panel end of the cable to the LCD panel connector.
3. **TTL type panels:** Plug the signal cables direct to CN2 or CN3. Plug the other end of cables to the LCD connector board (if connector board is required, otherwise the signal can be direct plug to the LCD panel connector). Then plug the board connector to the LCD panel connector.
4. **Inverter & Controller:** Plug the inverter cable to CNB1 and CNA1 (if necessary). Plug another end to the connector on the inverter.
5. **Function switch & Controller:** Plug the OSD switch mount cable to CNC1 on the controller board and another to the OSD switch mount.
6. **LED & Controller:** Plug in a 3-way with dual colour LED to connector LED1 on the controller board.
7. **Jumpers :** Check all jumpers are set correctly. Details referring the connection diagram at <http://www.digitalview.com/controllers/csg.php>
8. **Jumpers & Inverter & Panel voltage:** Particularly pay attention to the settings of JA3, JB2, JB3, JA5. JB2 & JB3 are used for inverter control (read inverter specification and information on the jumper table to define the correct settings). JA3 & JA5 are used for panel voltage input (read panel specification and information on the jumper table to define the correct settings).
9. **DVI cable :** Plug the DVI cable to the connector P2 on the controller board.
10. **VGA cable & Controller:** Plug the VGA cable to the connector P1 on the controller board.
11. **Power supply & Controller:** Plug the DC 12V power in to the connector PP2. You can consider to use DigitalView mating power cable P/N 426013800-3, 160mm.
12. **Power on:** Switch on the controller board and panel by using the OSD switch mount.

CAUTION: Never connect or disconnect parts of the display system when the system is powered up as this may cause serious damage.

LED status :

State	LED color
No signal & backlight off	RED
No signal & backlight on	ORANGE
With signal & backlight on	GREEN

General:

- If you are using supplied cables & accessories, ensure they are correct for the model of panel and controller.
- If you are making your own cables & connectors refer carefully to both the panel & inverter specifications and the section in this manual, "Connectors, Pinouts & Jumpers" to ensure the correct pin to pin wiring.

PC SETTINGS

The controller has been designed to take a very wide range of input signals however to optimize the PC's graphics performance we recommend choosing 60Hz vertical refresh rate – this will not cause screen flicker.

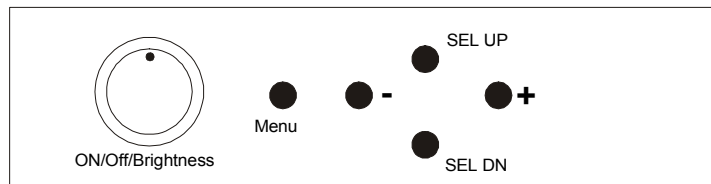
OPERATION

Once the system has been connected and switched on there are a number of functions available to adjust the display image as summarized in the following sections. The settings chosen will be saved for each mode independently.

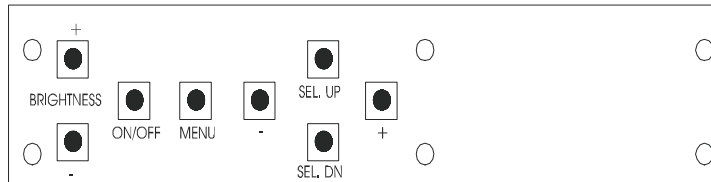
LCD DISPLAY SYSTEM SETTINGS

NOTE: By way of explanation the following refers to a set of sample buttons that may be obtained as an option. In addition to power on/off and connection for backlight brightness the controller provides an On Screen Display of certain functions which are controlled by 5 momentary type buttons (analog VR type) or 8 momentary type buttons (digital type):

Controls	Analog VR type	Digital type
On/Off – turns controller board power on	VR toggle switch	On/Off button
Brightness – controls backlight brightness	Rotary VR	Brightness +/- buttons
Menu – turns OSD menu On or Off (it will auto time off)	Menu button	Menu button
Select – Select function / Confirm	SEL DN	SEL DN
Move up to select individual RGB color level OSD page	SEL UP	SEL UP
+ – increase the setting / moves the selector to the next function	+	+
-- decrease the setting / moves the selector to the previous function	-	-





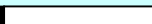


















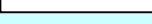






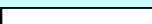

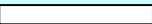

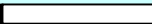





Analog VR type



Digital type

OSD Functions

				Select input source
		Input source 1	Select input source to Analog RGB	
		Input source 2	Select input source to DVI	
		Auto Source Seek	ON – Auto source select always enable OFF – Disable auto source select function	
		Wide screen mode information display	Select the input mode (1280 / 1360 / 1366 / 1368) to recognize and display the correct input signal information display on the OSD menu. 1280 : 1280x768 1360 : 1360x768 1366 : 1366x768 1368 : 1368x768	
		Exit	Exit the OSD menu and save the settings	
				Brightness and Contrast
		Brightness	Increase/decrease brightness level. Press – or + (- +) Total : 256 steps	
		Contrast	Increase/decrease panel contrast level. Press – or + (- +) Total : 192 steps	
		Exit	Exit the OSD menu and save the settings	
				Color
		Auto RGB Calibration	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	
		Color Temperature ▶	(Adjust the warmth of the image displayed. The higher temperature the coolest image looks like. The lower temperature the warmest image looks like.)	

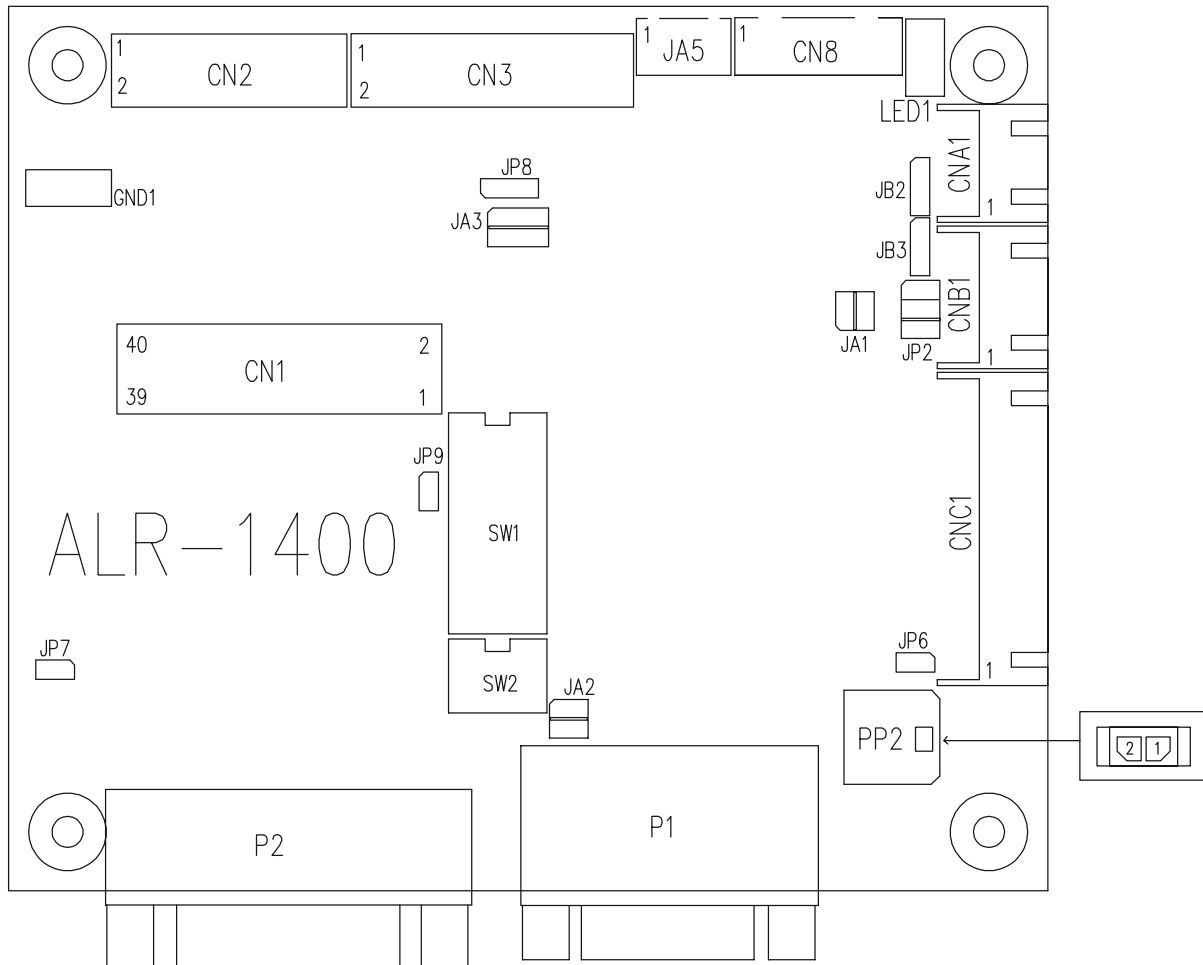
			Adjust red color level Press – or + (-  +) Total :128 steps Adjust green color level Press – or + (-  +) Total : 128 steps Adjust blue color level Press – or + (-  +) Total : 128 steps Press SEL UP/DN button to select item
			Set the color temperature to 4200K
			Set the color temperature to 5000K
			Set the color temperature to 6500K
			Set the color temperature to 7500K
			Set the color temperature to 9300K
		Gamma adjustment ▶	Adjust Gamma settings (0.4 / 0.6 / 1.0 / 1.6 / 2.2)
			Select Gamma to 0.4
			Select Gamma to 0.6
			Select Gamma to 1.0
			Select Gamma to 1.6
			Select Gamma to 2.2
		Exit	Exit the OSD menu and save the settings
	Position		
		Autosetup	Auto adjust the positions, phase, frequency <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No
		Frequency	Adjust the image horizontal size
		Phase	Fine tune the data sampling position (adjust image quality)
		Image Horizontal Position	Use +/- to move the image horizontally Press – or + (-  +)
		Image Vertical Position	Use +/- to move the image vertically Press – or + (-  +)
		Exit	Exit the OSD menu
	Utilities		
		OSD setting ▶	
			OSD Timeout : 1 - 15 seconds Press – or + (-  +)
			OSD menu horizontal position Press – or + (-  +)
			OSD menu vertical position Press – or + (-  +)
		Load Factory Default	Initialize the setting stored in non-volatile memory
		Sharpness	Adjust sharpness level Press – or + (-  +) Total : 7 steps
		Exit	Exit the OSD menu
	Exit the OSD menu		

[Firmware version : V1.45 or up]

Items marked ▶ have sub menus.
Exit the OSD menu to save the setting chosen

CONNECTORS, PINOUTS & JUMPERS

The various connectors are:



Summary: Connectors

Ref	Purpose	Description
CN1	LVDS panel signal	Hirose 40-pin, DF13-40DP-1.25DSA (Mating type : DF13-40DS-1.25C)
CN2	TTL panel signal	Hirose 40-pin, DF20G-40DP-1V (Mating type : DF20A-40DS-1C)
CN3	TTL Panel signal	Hirose 50-pin, DF20G-50DP-1V (Mating type : DF20A-50DS-1C)
CN8	Serial control (for firmware programming or RS-232 control use only)	JST 6-way, B6B-XH-A (Mating type : XHP-6)
CNA1	Auxiliary power output	JST 4-way, B4B-XH-A (Mating type : XHP-4)
CNB1	Backlight inverter	JST 5-way, B5B-XH-A (Mating type : XHP-5)
CNC1	Function controls	JST 12-way, B12B-XH-A (Mating type : XHP-12)
JA5	External panel power input	JST 3-way, B3B-XH-A (Mating type : XHP-3)
LED1	Dual color LED connector	Header pin 3x1
P1	ARGB signal input	DB-15 way high density 3 row
P2	DVI-D signal input	DVI-D connector
PP2	Power input	Molex 43650-0200 compatible (Mating type : Molex 43645-0200 compatible) (Matching power cable : P/N 426013800-3)

Summary: Jumpers setting

Ref	Purpose	Note
JA1	On board +5V power enable	1-2 & 3-4 closed, factory set, do not remove
JA2	On board +3.3V power enable	1-2 & 3-4 closed, factory set, do not remove
JA3	Panel power voltage select	1-3 & 2-4 = +3.3V panel voltage supply 3-5 & 4-6 = +5V panel voltage supply CAUTION: Incorrect setting will cause panel damage
JA5	+12V Panel power voltage select	Open = Disable +12V panel power 1-2 = +12V safe panel power on CN3 pin 48, 49, 50 2-3 = External panel power input CAUTION: Incorrect setting can damage panel
JB2	Backlight inverter on/off control – signal level	1-2 = On/Off control signal 'High' = +12V 2-3 = On/Off control signal 'High' = +5V Open = On/Off control signal 'High' = Open collector CAUTION: Incorrect setting can damage inverter.
JB3	Backlight inverter on/off control – polarity	1-2 = control signal 'high' = CCFT ON 2-3 = control signal 'low' = CCFT ON
JP2	Backlight control type selection	1-2 = VR/Digital switch mount control 3-4 = RS-232 backlight brightness control 5-6 = Reserved
JP6	Input power control	Short = External switch control Open = Switch mount control
JP7	Reserved	Reserved for internal programming use (Always 1-2 closed)
JP8	Clock phase (Analog RGB)	1-2 = Normal 2-3 = Invert
JP9	Reserved	1-2 = Default
SW1	Panel selection	See table below
SW2	Panel selection	See table below

DIP Switch selection – SW1

Pos #1	Pos #2	Pos #3	Pos.#4	Description
For SXGA panel				
ON	ON	ON	OFF	Sharp LQ181E1LW31 Fujitsu FLC48SXC8V
For WXGA panels				
ON	OFF	OFF	OFF	Samsung LTA260W2-L01
OFF	ON	OFF	OFF	NEC NL12876BC26-21
ON	ON	OFF	OFF	Samsung LTA460WS-L03 (Non-tested)*
OFF	OFF	ON	OFF	Sharp LQ315T3LZ24 (Non-tested)*
ON	OFF	ON	OFF	LG LC420W02-A4 (Non-tested)*
For XGA panel				
OFF	ON	ON	OFF	LG LM151X2
ON	ON	OFF	ON	Sharp LQ150X1LGB1 Sharp LQ150X1LGN2A Fujitsu FLC38XGC6V-06
ON	OFF	ON	ON	NEC NL10276BC12-02
For SVGA panel				
OFF	OFF	ON	OFF	Sharp LQ104S1DG21 Sharp LQ121S1DG41
OFF	ON	ON	OFF	Sharp LQ084S3DG01
ON	OFF	ON	OFF	Toshiba LTM12C289
For VGA panel				
ON	OFF	OFF	OFF	Sharp LQ104V1DG21 Sharp LQ10D368 Sharp LQ104V1DG51

For additional and recent added panels, see ALR-1400 panel support table at <http://www.digitalview.com/controllers/csg.php>

* Only effective on V1.31 or up version.

Pos #5	Pos #6	Pos #7	Description
OFF	OFF	OFF	Reserved
ON	OFF	OFF	Reserved
OFF	ON	OFF	SXGA
ON	ON	OFF	WXGA
OFF	OFF	ON	XGA
ON	OFF	ON	SVGA
OFF	ON	ON	VGA
ON	ON	ON	Reserved

SW1 Pos 8 = Reserved.

DIP switch selection – SW2

Pos. #	Function	Description
1	Panel pixel format	OFF : Double Pixel ON : Single Pixel
2	LVDS data mapping select	ON : Mapping A (LVDS panel) OFF : Mapping B (LVDS panel) Please adjust to get the correct picture. See as Appendix II for details of mapping A and B. Set ON for TTL panels.

The most current list can be found the controller solution generator at <http://www.digitalview.com/controllers/csg.php>

CN1 – Panel connector: Hirose, DF13A-40DP-1.25DSA (Matching type : DF13-40DS-1.25C)

PIN	SYMBOL	DESCRIPTION
1	TXA0+	Positive differential LVDS data bit A0
2	TXA0-	Negative differential LVDS data bit A0
3	TXA1+	Positive differential LVDS data bit A1
4	TXA1-	Negative differential LVDS data bit A1
5	Reserved	-
6	Reserved	-
7	TXA2+	Positive differential LVDS data bit A2
8	TXA2-	Negative differential LVDS data bit A2
9	TXA3+	Positive differential LVDS data bit A3
10	TXA3-	Negative differential LVDS data bit A3
11	GND	Ground
12	GND	Ground
13	TXAC+	Positive LVDS clock for A channel
14	TXAC-	Negative LVDS clock for A channel
15	GND	Ground
16	GND	Ground
17	TXB0+	Positive differential LVDS data bit B0
18	TXB0-	Negative differential LVDS data bit B0
19	TXB1+	Positive differential LVDS data bit B1
20	TXB1-	Negative differential LVDS data bit B1
21	Reserved	-
22	Reserved	-
23	TXB2+	Positive differential LVDS data bit B2
24	TXB2-	Negative differential LVDS data bit B2
25	TXB3+	Positive differential LVDS data bit B3
26	TXB3-	Negative differential LVDS data bit B3
27	GND	Ground
28	GND	Ground
29	TXBC+	Positive LVDS clock for B channel
30	TXBC-	Negative LVDS clock for B channel
31	GND	Ground
32	GND	Ground
33	VDD (3.3V/5V)	Panel power supply (3.3V/5V)
34	VDD (3.3V/5V)	Panel power supply (3.3V/5V)
35	VDD (3.3V/5V)	Panel power supply (3.3V/5V)
36	VDD (3.3V/5V)	Panel power supply (3.3V/5V)
37	NC	No connection
38	VDD +12V	Panel power supply (+12V)
39	VDD +12V	Panel power supply (+12V)
40	VDD +12V	Panel power supply (+12V)

CN2 – Panel connector: HIROSE DG20G-40DP-1V (Matching type : DF20A-40DS-1C)

PIN	SYMBOL	DESCRIPTION
1	GND	Ground
2	GND	Ground
3	NC	No connection
4	NC	No connection
5	ER0	Even data bit R0
6	ER1	Even data bit R1
7	ER2	Even data bit R2
8	ER3	Even data bit R3
9	ER4	Even data bit R4
10	ER5	Even data bit R5
11	ER6	Even data bit R6
12	ER7	Even data bit R7
13	GND	Ground
14	GND	Ground
15	NC	No connection
16	NC	No connection
17	EG0	Even data bit G0
18	EG1	Even data bit G1
19	EG2	Even data bit G2
20	EG3	Even data bit G3
21	EG4	Even data bit G4
22	EG5	Even data bit G5
23	EG6	Even data bit G6
24	EG7	Even data bit G7
25	GND	Ground
26	GND	Ground
27	NC	No connection

28	NC	No connection
29	EB0	Even data bit B0
30	EB1	Even data bit B1
31	EB2	Even data bit B2
32	EB3	Even data bit B3
33	EB4	Even data bit B4
34	EB5	Even data bit B5
35	EB6	Even data bit B6
36	EB7	Even data bit B7
37	GND	Ground
38	GND	Ground
39	CLK	Dot clock
40	NC	No connection

CN3 – Panel connector: HIROSE DF20G-50DP-1V (Matching type : DF20A-50DS-1C)

PIN	SYMBOL	DESCRIPTION
1	GND	Ground
2	GND	Ground
3	NC	No connection
4	NC	No connection
5	OR0	Odd data bit R0
6	OR1	Odd data bit R1
7	OR2	Odd data bit R2
8	OR3	Odd data bit R3
9	OR4	Odd data bit R4
10	OR5	Odd data bit R5
11	OR6	Odd data bit R6
12	OR7	Odd data bit R7
13	GND	Ground
14	GND	Ground
15	NC	No connection
16	NC	No connection
17	OG0	Odd data bit G0
18	OG1	Odd data bit G1
19	OG2	Odd data bit G2
20	OG3	Odd data bit G3
21	OG4	Odd data bit G4
22	OG5	Odd data bit G5
23	OG6	Odd data bit G6
24	OG7	Odd data bit G7
25	GND	Ground
26	GND	Ground
27	NC	No connection
28	NC	No connection
29	OB0	Odd data bit B0
30	OB1	Odd data bit B1
31	OB2	Odd data bit B2
32	OB3	Odd data bit B3
33	OB4	Odd data bit B4
34	OB5	Odd data bit B5
35	OB6	Odd data bit B6
36	OB7	Odd data bit B7
37	GND	Ground
38	GND	Ground
39	VS	Vertical sync
40	CLK	Dot clock
41	HS	Horizontal sync
42	DE	Display enable
43	PWR	Power down control signal (5v TTL)
44	VLCD	Panel power supply (3.3v/5v configurable)
45	VLCD	Panel power supply (3.3V/5v configurable)
46	VLCD	Panel power supply (3.3V/5v configurable)
47	NC	No connection
48	VLCD12	+12V panel supply (selected by JA5)
49	VLCD12	+12V panel supply (selected by JA5)
50	VLCD12	+12V panel supply (selected by JA5)

CN8 – RS-232 serial control: JST B6B-XH-A (Matching type : XHP-6)

PIN	SYMBOL	DESCRIPTION
1	SDATA	Reserved
2	SCLK	Reserved
3	VCC	+5V
4	TXD	RS-232 Tx data
5	GND	Ground
6	RXD	RS-232 Rx data

CNA1 - Auxiliary power output: JST B4B-XH-A (Matching type : XHP-4)

PIN	SYMBOL	DESCRIPTION
1	AUX 12V	+12V DC, 500mA max
2	GND	Ground
3	GND	Ground
4	AUX 5V	+5V DC, 500mA max

CNB1 – Backlight inverter connector: JST B5B-XH-A (Matching type : XHP-5)

PIN	SYMBOL	DESCRIPTION
1	GND	Ground
2	VBKL	Backlight power supply, +12VDC (switched)
3	BLCTRL	Backlight On/Off control signal (refer to JB2 & JB3)
4	BVR_WIP	Backlight brightness VR pin WIP
5	BVR_A	Backlight brightness VR pin A

CNC1 – Control switch, JST B12B-XH-A

PIN	SYMBOL	DESCRIPTION
1	PSWIN	Power button A
2	SW_ON	Power button B
3	BVR_A	Backlight Brightness VR pin A
4	BVR_WIP	Backlight Brightness R pin WIP
5	BVR_B	Backlight Brightness VR pin B (470 ohm resistor to +5V Vcc)
6	GND	Ground
7	MENU	OSD menu
8	-/LEFT	OSD -/Left
9	+ /RIGHT	OSD +/Right
10	SEL_DN	OSD Select down
11	SEL_UP	OSD Select up
12	NC	No connection

JA5 - Auxiliary power output, JST B4B-XH-A

PIN	SYMBOL	DESCRIPTION
1	PANEL_PWR_12V	Internal +12V panel power
2	PANEL_PWR_External	Panel power (12V~18 V use external power input cable)
3	GND	Ground (External power ground)

LED1 – Status LED connector: 3-pin header

PIN	DESCRIPTION
1	Green LED pin (anode)
2	LED pin common (cathode)
3	Red LED pin (anode)

P1 - Analog VGA input – DB-15 way high density 3 row

PIN	SYMBOL	DESCRIPTION
1	PCR	Red, analog
2	PCG	Green, analog
3	PCB	Blue analog
4	ID2	Reserved for monitor ID bit 2 (grounded)
5	DGND	Digital ground
6	AGND	Analog ground red
7	AGND	Analog ground green
8	AGND	Analog ground blue
9	DDC_5V	+5V power supply for DDC (optional)
10	DGND	Digital ground
11	ID0	Reserved for monitor ID bit 0 (grounded)
12	DDC_SDA	DDC serial data
13	HS_IN	Horizontal sync or composite sync, input
14	VS_IN	Vertical sync, input
15	DDC_SCL	DDC serial clock

P2 – DVI-D input

PIN	SYMBOL	DESCRIPTION
1	/RX2	TMDS Data 2-
2	RX2	TMDS Data 2+
3	GND	Digital Ground
4	NC	No connection
5	NC	No connection
6	DVI_DDC_CLK	DDC Clock
7	DVI_DDC_DAT	DDC Data
8	DVI_VS_IN	Analog vertical Sync
9	/RX1	TMDS Data 1-
10	RX1	TMDS Data 1+
11	GND	Digital Ground
12	NC	No connection
13	NC	No connection
14	DVI_DDC_5V	+5V power supply for DDC (optional)
15	GND	Ground (+5, Analog H/V Sync)
16	NC	No connection
17	/RX0	TMDS Data 0-
18	RX0	TMDS Data 0+
19	GND	Digital Ground
20	NC	No connection
21	NC	No connection
22	GND	Digital Ground
23	RXC	TMDS Clock+
24	/RXC	TMDS Clock-
25	NC	No connection
26	NC	No connection

PP2 - 12VDC power supply

PIN	DESCRIPTION
1	+12VDC
2	Ground

APPLICATION NOTES

USING THE CONTROLLER WITHOUT BUTTONS ATTACHED

This is very straightforward by following the steps below :

- Firstly setup the controller/display system with the buttons. With controls attached and display system active make any settings for colour and image position as required then switch everything off.
- Use a jumper to close JP6 jumper, this will fix the board On.
- Refer to inverter specifications for details as to fixing brightness to a desired level, this may require a resistor, an open circuit or closed circuit depending on inverter.

INVERTER CONNECTION

There are potentially 3 issues to consider with inverter connection:

- Power
- Enable
- Brightness

Please read the following sections for a guide to these issues.

Inverter Power: As per the table for CNB1 pin 1 is ground and pin 2 provides 12V DC. This should be matched with the inverter specification: see table.

CNB1

PIN	DESCRIPTION
1	Ground
2	+12VDC

Remark: For higher power inverter, more current (for 12V) can be taken from CNA1 pin 1.

Enable: This is a pin provided on some inverters for On/Off function and is used by this panel controller for VESA DPMS compliance. If the inverter does not have an enable pin or the enable pin is not used then DPMS will not be operational. Pin 3 should be matched to the inverters specification for the 'enable' or 'disable' pin.

CNB1

PIN	DESCRIPTION
3	Enable

Further, jumpers JB2 & JB3 should be set to match the inverters specification for the enable pin power and High or Low setting: see table.

Ref	Purpose	Note
JB2	Inverter enable voltage	1-2 H = 12V, 2-3 H = 5V (Vcc), OPEN H = open collector
JB3	Inverter control	1-2 H = On, 2-3 L = On

Brightness: There are various methods for brightness control and it is important to consider the specifications for the inverter to be used. Generally the situation is:

- Brightness can controlled by using a resistor or VR (Variable Resistor).
- Brightness controlled by adding a circuit such as PWM (Pulse Width Modulation).
- No adjustment of brightness is possible.

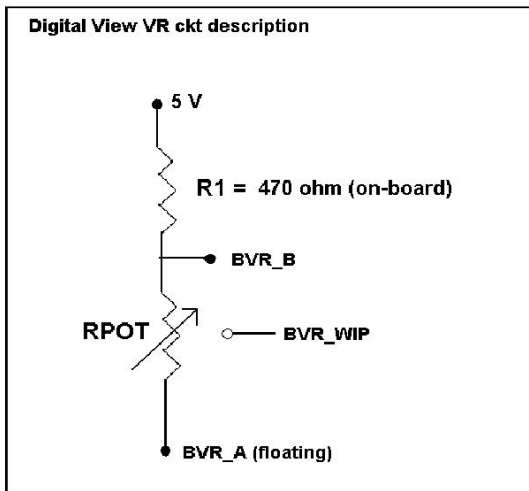
CNB1 pins 4 & 5 are available for connecting to an inverter or circuit where VR control is supported.

CNB1

PIN	DESCRIPTION
4	VR WIP
5	VR A

This can then be matched with function controls (OSD switch mount) pins 3 & 4: see cable design below .

Design Guideline for making VR circuitry :



Signal description / Notes :

- 1) R1 : 470ohm on board
- 2) RPOT is an external potentiometer (in-line dip style) that can be plugged directly into CNC1 pins 3,4,5. RPOT must be supplied / installed by user.
- 3) BVR_B : Voltage tapped from “top” of potentiometer, the node of R1 and RPOT.
- 4) BVR_WIP : Voltage tapped from wiper arm of RPOT.
- 5) BVR_A : Voltage tapped from “bottom” of RPOT.

Note : BVR_A voltage is left floating on the controller board. To use this circuit, you need to tie this point to a potential (usually GND, available at CNC1 pin 6).

CNB1 – Backlight inverter connector: JST B5B-XH-A (Matching type : XHP-5)

PIN	SYMBOL	DESCRIPTION
1	GND	Ground
2	VBKL	+12VDC, backlight power supply
3	BLCTRL	On/Off control (enable) – see JB2 & JB3
4	BVR_WIP	Brightness VR - WIP
5	BVR_A	Brightness VR A

CNC1 – Control switch, JST B12B-XH-A (Matching type : XHP-12)

PIN	SYMBOL	DESCRIPTION
1	PSWIN	Power button A
2	SW_ON	Power button B
3	BVR_A	Backlight Brightness VR pin A
4	BVR_WIP	Backlight Brightness R pin WIP
5	BVR_B	Backlight Brightness VR pin B (470 ohm resistor to +5V Vcc)
6	GND	Ground
7	MENU	OSD menu
8	-/LEFT	OSD -/Left
9	+ /RIGHT	OSD +/Right
10	SEL_DN	OSD Select down
11	SEL_UP	OSD Select up
12	NC	No connection

The VR for brightness depends on the inverter. The main power load for On/Off is handled by a relay on the controller.

Example for circuit design :

- 1.) Choose RPOT = 10K
- 2.) Tie BVR_A to GND
- 3.) Circuit analysis gives BVR_WIP as the following (see Figure 1)

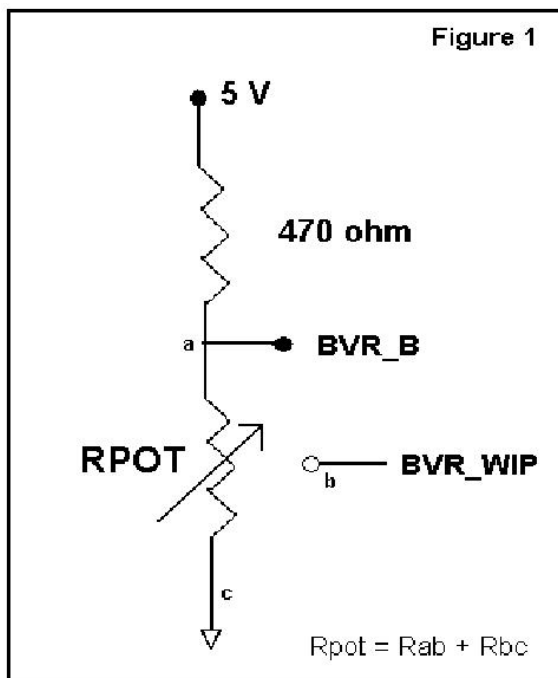
$$BVR_WIP = 5 \times (Rbc/10.47)$$

where BVR_WIP is in Volts.
And Rbc is the resistance from the wiper arm to bottom of pot in Kohms.

To evaluate, plug in different values of Rbc :

Rbc	BVR_WIP
0	0 V
2.5 K	1.2 V
5 K	2.4 V
7.5 K	3.6 V
10 K	4.8 V

So this circuit could provide Brightness adjust voltage ranging from 0V to 5V.



TROUBLESHOOTING

General

A general guide to troubleshooting a flat panel display system it is worth considering the system as separate elements, such as:

- Controller (jumpers, PC settings)
- Panel (controller, cabling, connection, panel, PC settings)
- Backlight (inverter, cabling, backlight tubes)
- Cabling
- Computer system (display settings, operating system)

Through step by step cross checking with instruction manuals and a process of elimination to isolate the problem it is usually possible to clearly identify the problem area.

No image:

- If the panel backlight is not working it may still be possible to just see some image on the display.
- A lack of image is most likely to be caused by incorrect connection, lack of power, failure to provide a signal or incorrect graphic card settings.

Image position:

If it is impossible to position the image correctly, ie the image adjustment controls will not move the image far enough, then test using another graphics card. This situation can occur with a custom graphics card that is not close to standard timings or if something is in the graphics line that may be affecting the signal such as a signal splitter (please note that normally a signal splitter will not have any adverse effect).

Image appearance:

- A faulty panel can have blank lines, failed sections, flickering or flashing display
- Incorrect graphics card refresh rate, resolution or interlaced mode will probably cause the image to be the wrong size, to scroll, flicker badly or possibly even no image.
- Incorrect jumper settings on the controller may cause everything from total failure to incorrect image. CAUTION: Do not set the panel power input incorrectly.
- Sparkling on the display: faulty panel signal cable.

Backlight:

Items to check include: Power input, Controls, Inverter and Tubes generally in this order.

If half the screen is dimmer than the other half:

- Check cabling for the inverter.
- For a specific backlight tube check the AC pins orientation (CAUTION: Never reverse any DC power pins).

Also:

- If adjusting brightness control has no effect the chances are that the VR rating or method of adjusting brightness is not compatible or correctly connected to the inverter.
- If system does not power down when there is a loss of signal

Continued failure:

If unit after unit keeps failing consider and investigate whether you are short circuiting the equipment or doing something else seriously wrong.

Generally after common sense issues have been resolved we recommend step by step substitution of known working parts to isolate the problem.

SPECIFICATIONS

Panel compatibility	1366x768, 1280x1024, 1280x768, 1024 x 768, 800x600, 640x480 TFT LVDS or TTL LCD's support.
No. of colours	Up to 3 x 8 bit providing 16.7 million colours.
Vertical refresh rate	SXGA, XGA, SVGA, VGA resolution up to 60Hz.
Dot clock (pixel clock) maximum	135 MHz
Graphics formats	Standard SXGA, XGA,SVGA,VGA
Standard input at source	- VGA analog (15-pin) standard with automatic detection of : Digital Separate Sync Sync-On-Green Composite Sync - DVI-D
Controls available	- On/Off - Brightness (inverter) - OSD menu, - OSD select - OSD setting + - OSD setting -
Control interface	- Buttons
Settings memory	Settings are stored in non volatile memory
Language OSD support	Graphics OSD icons
VESA DPMS implementation	Yes
Plug & Play	VESA DDC 1, 2/b compatible
Voltage output for LCD	+3.3V , +5V, +12V, +12~+18V (External voltage supply)
Input voltage	12VDC
Controller power consumption	Approx 4W (controller logic only, no panel and inverter are involved)
Controller dimensions	107mm x 92mm x 21mm
Storage temperature limits	-40°C to +70°C
Operating temperature limits	0°C to +60°C

NOTES

Please note the following:

- For specific panel setup a sample of an LCD may be required (this will be returned) and a copy of the full technical specifications for the panel from the manufacturer.
- Relayout and custom development services are available.

Appendix I – Graphic Mode Support Table

Mode	Resolution	Clk [MHz]	Horizontal freq [KHz]	Vertical freq [Hz]	Sync Mode
E1_70	640x350 70Hz	25.175	31.469	70.087	Digital Separate Sync
E2_70	640x400 70Hz	25.175	31.469	70.087	Digital Separate Sync
V_60	640x480 60Hz	25.175	31.469	59.940	Digital Separate Sync
V_60	640x480 60Hz	25.175	31.469	59.940	Sync On Green
V_60	640x480 60Hz	25.175	31.469	59.940	Composite Sync
SV_56	800x600 56Hz	36.000	35.156	56.250	Digital Separate Sync
SV_56	800x600 56Hz	36.000	35.156	56.250	Sync On Green
SV_56	800x600 56Hz	36.000	35.156	56.250	Composite Sync
SV_60	800x600 60Hz	40.000	37.879	60.317	Digital Separate Sync
SV_60	800x600 60Hz	40.000	37.879	60.317	Sync On Green
SV_60	800x600 60Hz	40.000	37.879	60.317	Composite Sync
X_60	1024x768 60Hz	65.000	48.363	60.004	Digital Separate Sync
X_60	1024x768 60Hz	65.000	48.363	60.004	Sync On Green
X_60	1024x768 60Hz	65.000	48.363	60.004	Composite Sync
SX_60	1280x1024 60Hz	108	63.81	60.020	Digital Separate Sync
SX_60	1280x1024 60Hz	108	63.81	60.020	Sync On Green
SX_60	1280x1024 60Hz	108	63.81	60.020	Composite Sync

Remark :

The controller has been designed to take a very wide range of input signals however to optimize the PC's graphics performance we recommend choosing 60Hz vertical refresh rate. To support on higher refresh rate over 60Hz, the LCD panel may not support.

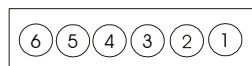
Appendix II – RS-232 control protocols

RS-232 Serial control (Baud rate 2400, 8 bits, 1 stop bit and no parity)

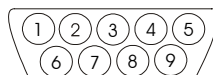
Physical connection :

Controller side
Connector interface : CN8
Mating connector : JST XHP-6

Computer side
Connector interface : Serial port
Mating connector : DB9 Female



Mating face of CN8



Mating face of RS-232 DB9 Male

PIN#	Description
4	RS-232 Tx Data
5	Ground
6	RS-232 Rx Data

PIN#	Description
2	RS-232 Rx Data
3	RS-232 Tx Data
5	Ground

Remark :

(1) : RS-232 connection cable, 600mm P/N 4260902-00 can be ordered separately for connection.

Software connection :

The OSD function can be controlled through sending the RS-232 protocol.

The RS-232 program can be custom-made to fit for application or it can be used the program provided by Digitalview on request. Please contact your local sales for informations.

1. Commands to implement switch mount control buttons

Function	Command	Description	Remark
Menu button	0xf7	Menu button pressed	Button equivalent
Select-down button	0xfa	Select-down button pressed	Button equivalent
Select-up button	0xfb	Select-up button pressed	Button equivalent
Right/+ button	0xfc	Right/+ button pressed	Button equivalent
Left/- button	0xfd	Left/- button pressed	Button equivalent

2. Parameter setting - immediate, relative, reset and query

Function	Command	Description	Acknowledge (if enabled)
Brightness control	0x81, nn "+" "-" "r" "R" "?"	Set brightness = value/increment/decrement Reset Query	Set Brightness Range : "0"0"-F"0 Default : "8"0 Each step interval is in 1
Contrast control	0x82, "a" "A", nn "+" "-" "r" "R" "?"	Set all contrast = value/increment/decrement Reset Query	Set Contrast Range : "4"0"-F"0 Default : "8"0 Each step interval is in 1
Phase	0x85, nn "+" "-" "?"	Set dot clock phase = value/increment/decrement Query	Dot clock phase. (In PC mode only)
Image H position	0x86, nnnn "+" "-" "?"	Set img_hpos = value/increment/decrement Query	Image horizontal position.
Image V position	0x87, nnnn "+" "-" "?"	Set img_vpos = value/increment/decrement Query	Image vertical position.
Frequency	0x8b, nnnn "+" "-" "?"	Set H active size = value/increment/decrement Query	Graphic mode H active size (in pixels)
Colour temperature select	0xb3, n "r" "R" "?"	Select colour temperature = value Reset Query	Main selected. "0" – user defined RGB values. "1" – 4200K. "2" – 5000K.

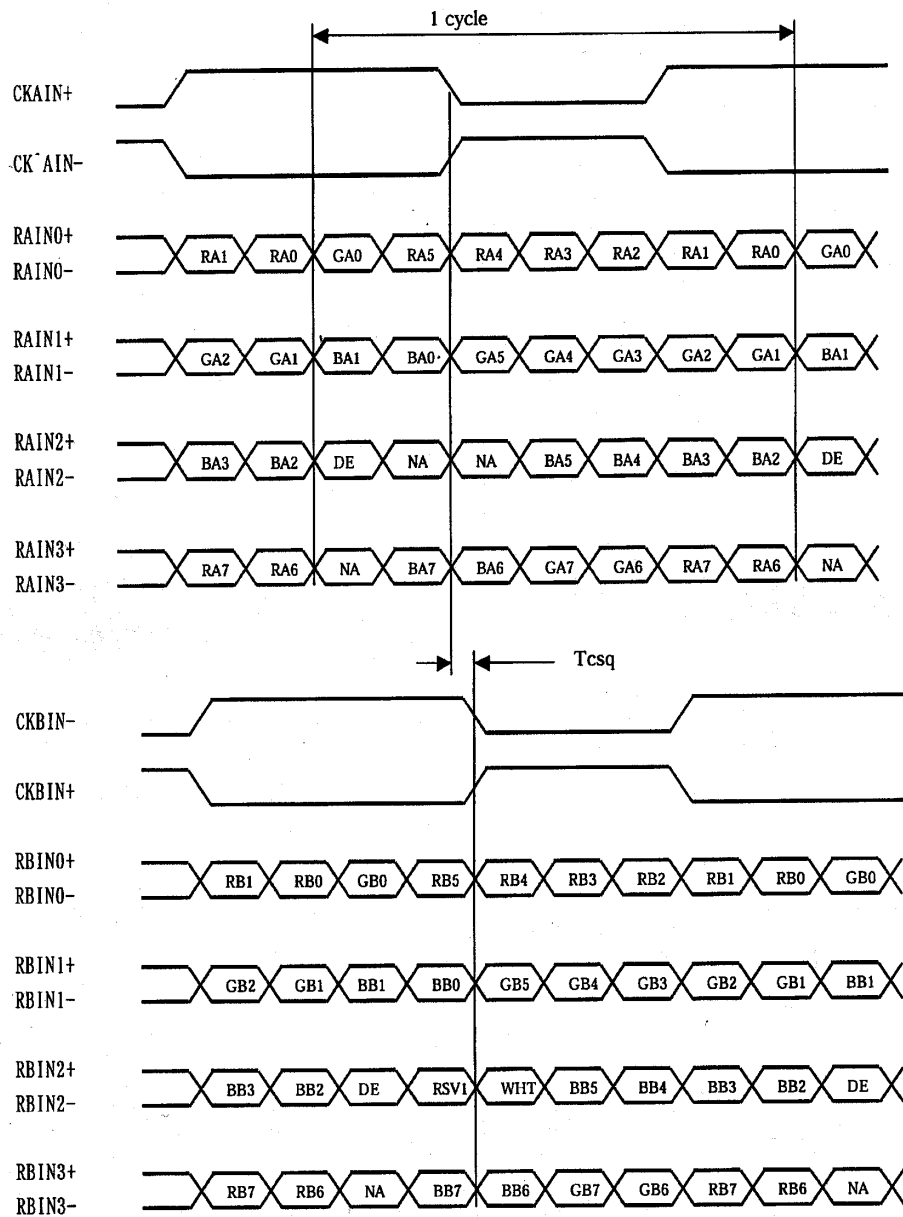
			"3" – 6500K. "4" – 7500K. "5" – 9300K.
Red level for selected colour temperature	0xb4, nn "+" "-" "r" "R" "?"	Set the level of the red channel for the selected colour temp. = value/increment/decrement Reset Query	Red level for selected colour temperature. Range : "8"0"-“F”F” Default : “F”F”
Green level for selected colour temperature	0xb5, nn "+" "-" "r" "R" "?"	Set the level of the green channel for the selected colour temp. = value/increment/decrement Reset Query	Green level for selected colour temperature. Range : "8"0"-“F”F” Default : “F”F”
Blue level for selected colour temperature	0xb6, nn "+" "-" "r" "R" "?"	Set the level of the blue channel for the selected colour temp. = value/increment/decrement Reset Query	Blue level for selected colour temperature. Range : "8"0"-“F”F” Default : “F”F”
OSD turn off	0xbd	Turn off the OSD.	"1" – successful.
Backlight Brightness control	0xe0 nn "+" "-" "r" "R" "?"	Set brightness = value/increment/decrement Reset Query	Set backlight brightness Range : "0"0"-“F”F” Default : “F”F” e.g "1"0" → 0xe0 0x31 0x30 * Need to short JP2 to 3-4 closed for enabling the backlight brightness control. * Apply for inverter control voltage in range of 0~5V. Each step interval is in 1
Backlight on/off control	0xe1, "0" "1" "r" "R" "?"	Set backlight brightness = Disable backlight Enable backlight Reset (Default = Backlight on) Query	Backlight on/off.

Other control

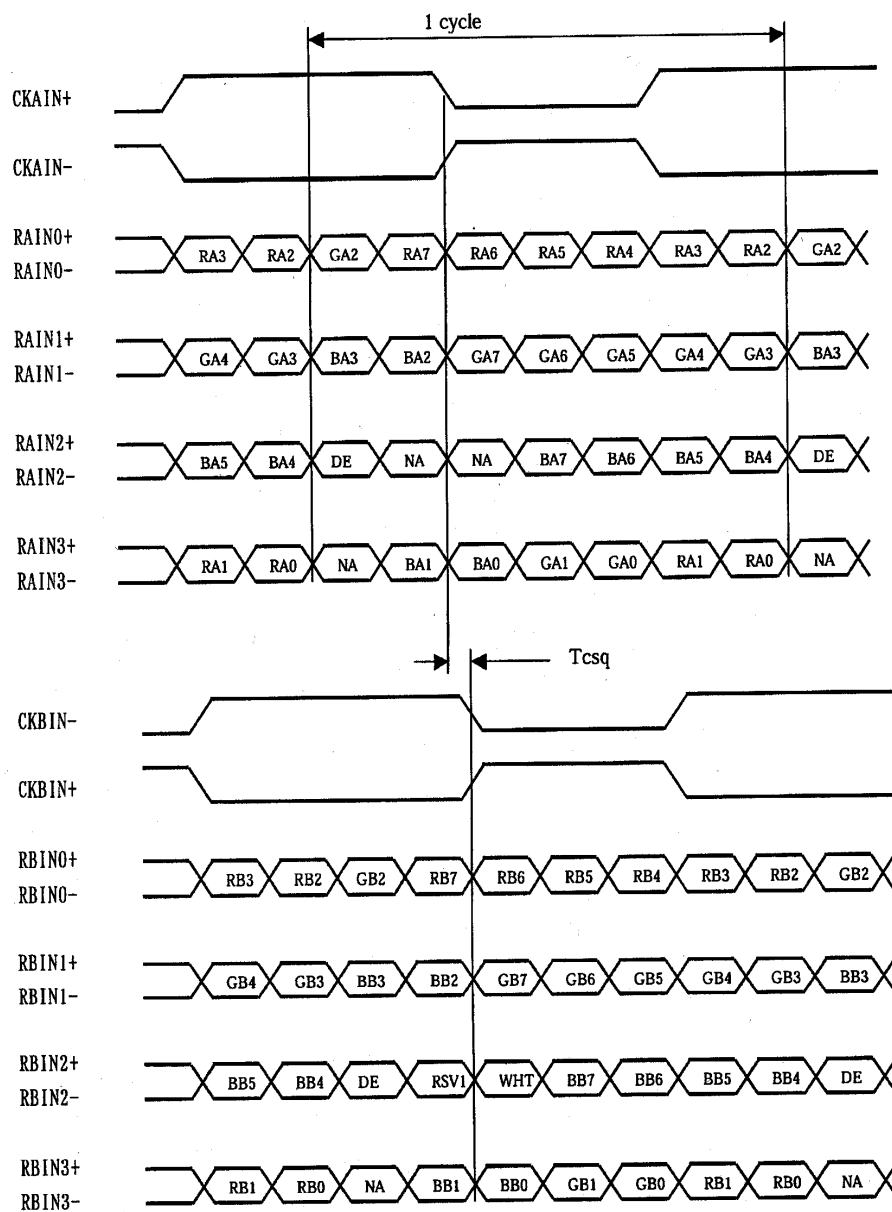
Function	Command	Description	Acknowledge (if enabled)
Select RS-232 acknowledge	0xc1, "0" "1"	Disable/enable command acknowledge.	"0" – acknowledge disabled. "1" – acknowledge enabled.
Auto-setup	0xc3	Start auto-setup of current vmode.	"0" – fail. "1" – successful.
Auto RGB Calibration	0xc5	Start auto-calibration of gain of the RGB amplifier.	"0" – fail. "1" – successful.
Load factory defaults	0xce	Reset all parameters to default value	"1" – successful.

Appendix III – Mapping definition

- Definition of Mapping A :



- Definition of Mapping B :



WARRANTY

The products are warranted against defects in workmanship and material for a period of three (3) year from the date of purchase provided no modifications are made to it and it is operated under normal conditions and in compliance with the instruction manual.

The warranty does not apply to:

- Product that has been installed incorrectly, this specifically includes but is not limited to cases where electrical short circuit is caused.
- Product that has been altered or repaired except by the manufacturer (or with the manufacturer's consent).
- Product that has subjected to misuse, accidents, abuse, negligence or unusual stress whether physical or electrical.
- Ordinary wear and tear.

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Whilst care has been taken to provide as much detail as possible for use of this product it cannot be relied upon as an exhaustive source of information. This product is for use by suitably qualified persons who understand the nature of the work they are doing and are able to take suitable precautions and design and produce a product that is safe and meets regulatory requirements.

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